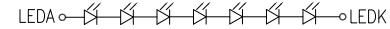
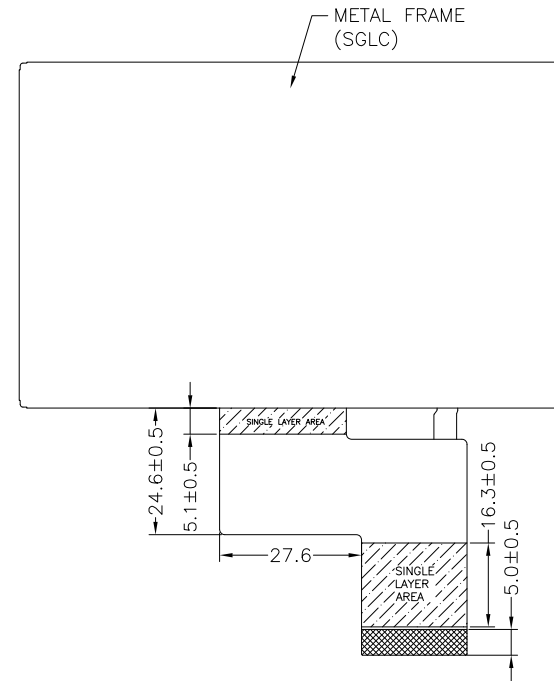


DETAIL A
(SCALE 50:1)



BACKLIGHT CIRCUIT DIAGRAM

PIN ASSIGNMENT

PIN	SYMBOL
1	LEDK
2	LEDA
3	GND
4	RESET
5	GND
6	CS
7	WRX
8	RDX
9	DCX
10	GND
11	D0
12	D1
13	D2
14	D3
15	D4
16	D5
17	D6
18	D7
19	D8
20	D9
21	D10
22	D11
23	D12
24	D13
25	D14
26	D15
27	GND
28	TE
29	VCI
30	IOVCC
31	NC
32	NC
33	NC
34	NC
35	NC
36	NC
37	NC(YU)
38	NC(XL)
39	NC(YD)
40	NC(XR)

NOTES:

1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY BLACK
2. OPERATING VOLTAGE: VCI=3.3V (TYP.)
3. VIEWING DIRECTION: FULL VIEWING ANGLE
4. DRIVER IC: NV3041
5. OPERATING TEMP.: -20°C ~ 70°C
6. STORAGE TEMP.: -30°C ~ 80°C
7. LED BACKLIGHT: WHITE LED, IF=20mA, VF=19.6V~23.8V
8. SURFACE LUMINANCE: 250CD/M² (TYP.)
9. TOP POLARIZER SURFACE TREATMENT: ANTI-GLARE
10. ONLY THE SINGLE LAYER AREA ON FPC IS BENDABLE AND THE MINIMUM BEND RADIUS IS 1.5MM
11. GENERAL TOLERANCE: ±0.3MM
12. ROHS COMPLIANT

MIT MULTI-INNO TECHNOLOGY CO., LTD.

DRAWN BY:	RITA	2023.07.04		MODULE P/N:
CHECKED BY:			SCALE : 1/1	MI0430ALT-1
APPROVED BY:			UNIT: mm	DESCRIPTION:
VERSION NO:	01		SHEET NO.: 1/1	TFT MODULE

01	FIRST ISSUE	RITA	2023.07.04
VER.	REVISED DESCRIPTION	REVISER	DATE